# PGCB Tool Waivers

This document’s purpose is to describe known waivers for the PGCB for tools such as 0in and spyglass\_lp. These waivers should NOT be used as is, but should be modified such that they are specific to the IP where they are used (update module-instance names, hierarchies, clock groups, etc). The final waivers should also be reviewed with the IP’s micro-architects.

**Note**: lintra waivers are located in tools/lint/waivers/pgcbunit.lwv and may be used as-is.

## 0in/CDC

**## pgcb\_force\_rst\_b only toggles when the clocks of the destination flops are gated**

cdc report crossing –tx\_clock <pgcb\_clk> -from pgcbunit\*.i\_pgcbfsm1.pgcb\_force\_rst\_b -severity waived

**## If the IP design guarantees that the inputs to the isolation gates are at the isolation clamped values when pgcb\_isol\_en\_b toggles, this waiver can be used**

cdc report crossing –tx\_clock <pgcb\_clk> -from pgcbunit\*.i\_pgcbfsm1.pgcb\_isol\_en\_b -severity waived

**## If the inputs to the isolation latches are state retention cells, this waiver may be used as the clocks will be gated when the latches are closed, and the input to the latch will equal the latch value when the latch is opened.**

cdc report crossing –tx\_clock <pgcb\_clk> -from pgcbunit\*.i\_pgcbfsm1.pgcb\_isol\_latchen -severity waived

**# DFx overrides are asynchronous to the pgcb\_clk but will be stable during functional mode**

cdc report crossing -scheme no\_sync -tx\_clock PGCB\_TCK -through \*i\_pgcbdfxovr1.dfxovr\_isol\_en\_b -severity waived -module pgcbunit

cdc report crossing -scheme no\_sync -tx\_clock PGCB\_TCK -through \*i\_pgcbdfxovr1.dfxovr\_isol\_latchen -severity waived -module pgcbunit

cdc report crossing -scheme no\_sync -tx\_clock PGCB\_TCK -through \*i\_pgcbdfxovr1.dfxovr\_force\_rst\_b -severity waived -module pgcbunit

cdc report crossing -scheme no\_sync -tx\_clock PGCB\_TCK -through \*i\_pgcbdfxovr1.dfxovr\_sleep\* -severity waived -module pgcbunit

**# DFx combi logic on POK is stable in functional mode and will not introduce glitches**

cdc report crossing -scheme combo\_logic -through \*i\_pgcbdfxovr1.dfxovr\_pok -through pgcb\_pok -severity waived -module pgcbunit

**# logic on fet\_en\_b but ctech\_mux will be glitch free when switching if both inputs to mux are equal**

cdc report crossing -scheme no\_sync -through \*i\_pgcbdfxovr1.dfxovr\_fet\_en\_b -severity waived -module pgcbunit

## Spyglass\_lp

**##DFx logic is static during functional mode**

waive -file "<filename>" -rule "LPCONN04B" -msg "'Save' signal '\*.pgcb\_sleep' of domain (domain '\*') is not driven by a port or a flop output pin"

waive -file "<filename>" -rule "LPCONN04B" -msg "'Isolation' signal '\*.pgcb\_isol\_en\_b' of domain (domain '\*') is not driven by a port or a flop output pin"

waive -file "<filename>" -rule "LPCONN04B" -msg "'Power-Switch enable' signal '\*.pgcb\_ip\_fet\_en\_b' of domain (domain '\*') is not driven by a port or a flop output pin"

waive -file "<filename>" -rule "LPSVM12B" -msg "Isolation signal '\*.pgcb\_isol\_en\_b' is not a state signal"